

Electronic Acknowledgement Receipt

EFS ID:	9251781
Application Number:	10738496
International Application Number:	
Confirmation Number:	7841
Title of Invention:	STRAINED SILICON MOSFETS HAVING NMOS GATES WITH WORK FUNCTIONS FOR COMPENSATING NMOS THRESHOLD VOLTAGE SHIFT
First Named Inventor/Applicant Name:	Ihsan J. Djomehri
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Filer:	Arthur J. Steiner/Brandy Stoeckel
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Attorney Docket Number:	039153-0688
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Application Type:	Utility under 35 USC 111(a)

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Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1		SB80_SB96_SerialNo10738496 _filed01-14-11.pdf	632355 <small>Ba7d8Edc7c54f0ce78ba97a802222e9f03c ae4</small>	yes	2

Multipart Description/PDF files in .zip description		
Document Description	Start	End
Power of Attorney	1	1
Assignee showing of ownership per 37 CFR 3.73(b).	2	2

Warnings:

Information:

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